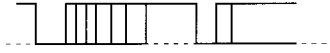
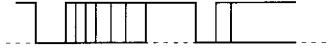



TP	Name	Signal	Freq/Per	Description
215	+5VMA	+5V	DC	M-ASIC supply
225	+5VFL	+5V	DC	FlashROM supply
230	READY		varying	$\mu$ P ready input to lengthen external memory cycles; low pulses $\geq 0.15 \mu$ s
231	$\mu$ RESETN	+5V	DC	Microprocessor reset; low during reset
232	INST		varying	ROM instruction fetch; low pulses $\geq 0.15 \mu$ s
235	+5VDA	+5V	DC	Display control circuit supply
300	-VBAT	-4.8V	DC	Nominal battery voltage (without mains adapter)
		-4.3V $\pm$ 0.15V		"battery low" warning
		-3.9V $\pm$ 0.15V		ScopeMeter switches off
301	BAT-LEVEL	+3.2V	DC	At nominal battery voltage (without mains adapter)
		+2.9V $\pm$ 0.1V		"battery low" warning
		+2.65V 0.1V		ScopeMeter switches off
310	-		4 ms	RAM POWER oscillator signal at power off; +0.8V DC at power on
311	+VRAM	+3.4V	DC	RAM POWER output at power off
		+5V	DC	RAM POWER output at power on
330	VPP	+12V	DC	FlashROM programming voltage, to be supplied in battery compartment
340	RXD	+5V or 0V	DC	Optical interface receiver H1340 dark: +5V, light: 0V
350	TXD	+5V or pulses	-	Optical interface transmitter Not sending: +5V, sending: pulses